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PATENT  
P56905

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

YOSHITAKA TERAOKA, *et al.*

Serial No.: 10/629,793

Examiner: WILLIAMS, JOSEPH L.

Filed: 30 July 2003

Art Unit: 2879

For: PLASMA DISPLAY PANEL AND MANUFACTURING METHOD THEREOF  
WHERE ADDRESS ELECTRODES ARE FORMED BY DEPOSITING A LIQUID  
IN CONCAVE GROOVES ARRANGED IN A SUBSTRATE (*as amended*)

**REQUEST FOR CORRECTED PTOL 85 (Notice of Allowance)**

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